

FIGURE 1

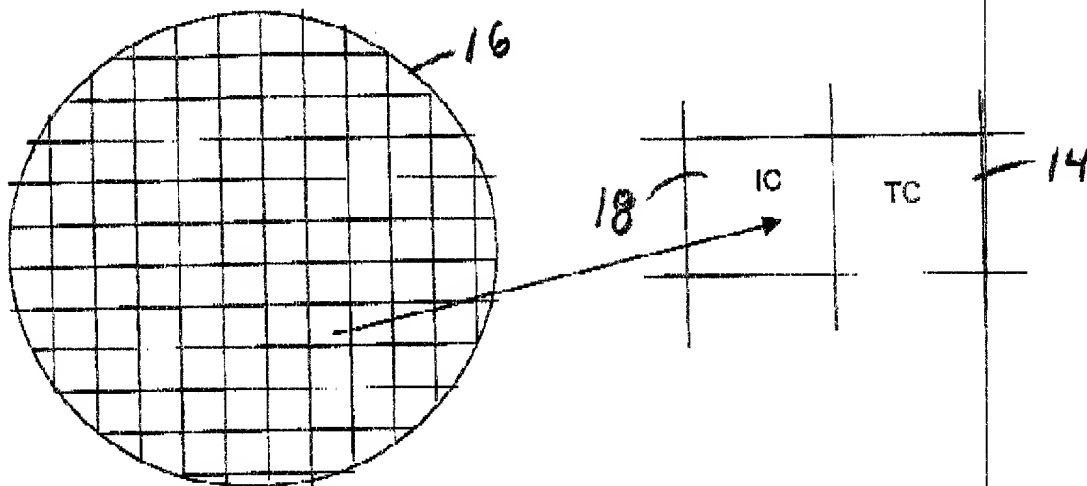


FIGURE 2

FIGURE 3

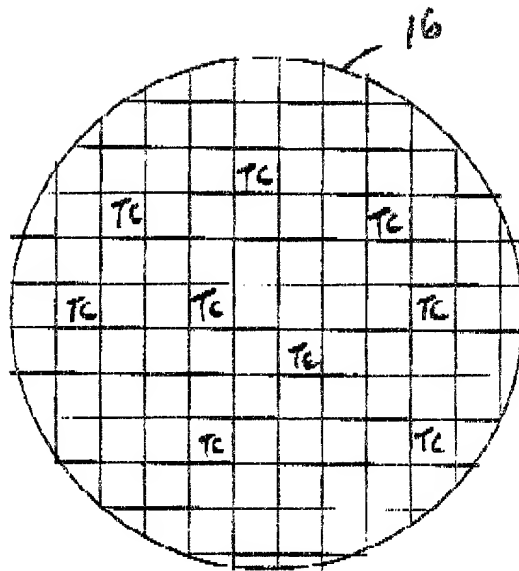


FIGURE 4

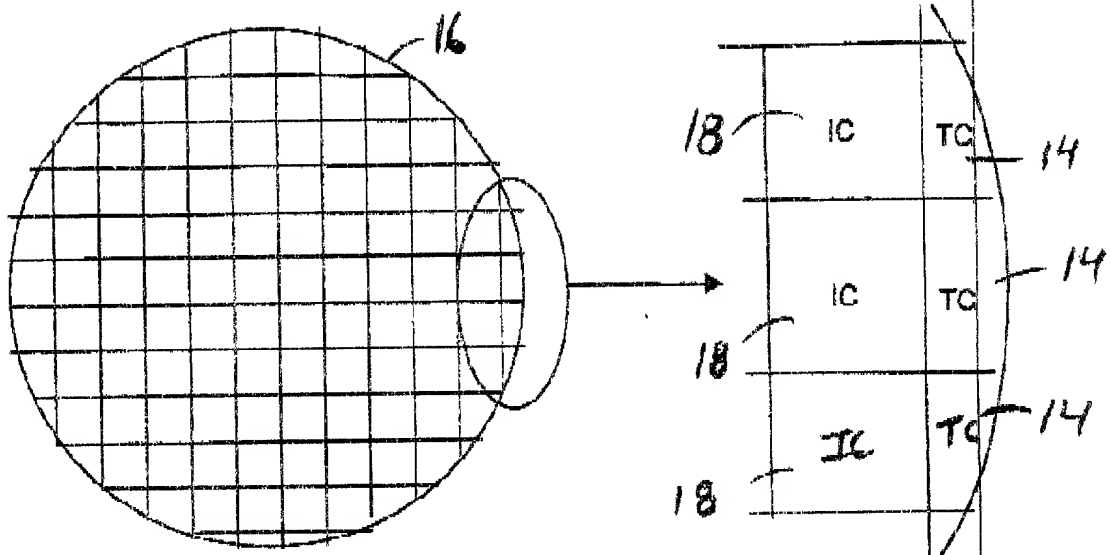


FIGURE 5

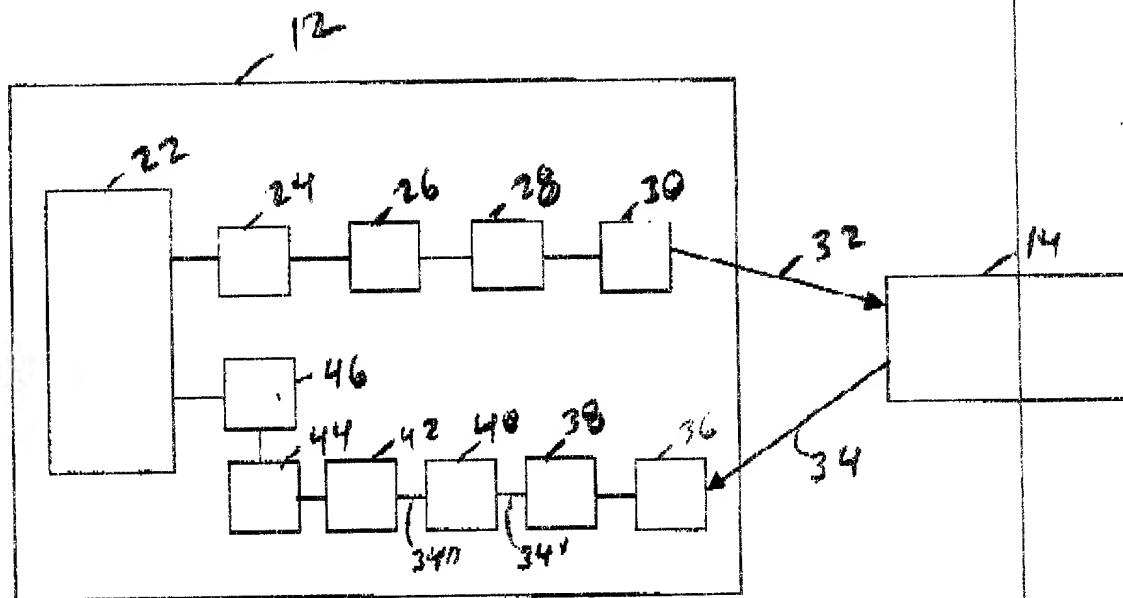


FIGURE 6

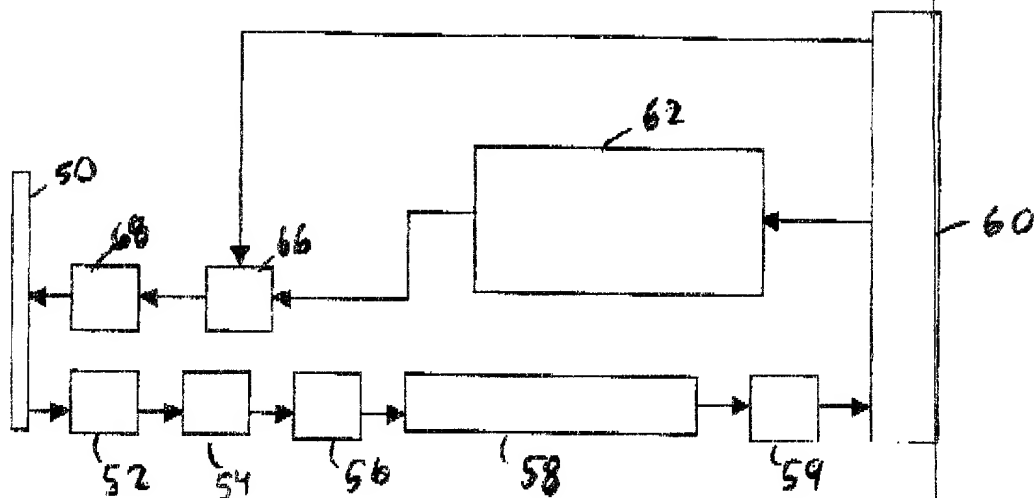


FIGURE 7.

FIG. 7

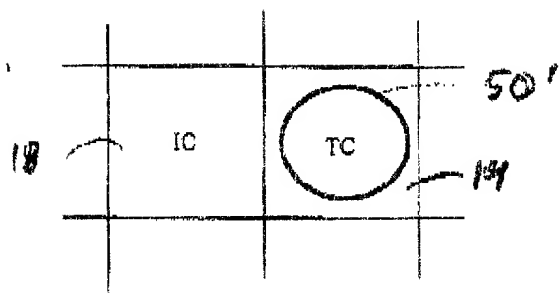


FIGURE 8

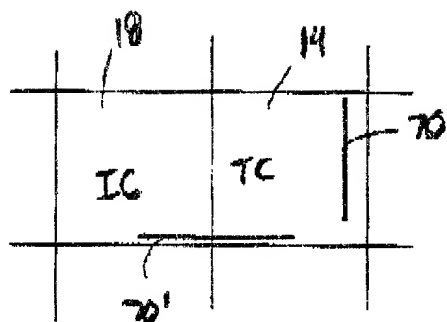


FIGURE 9a

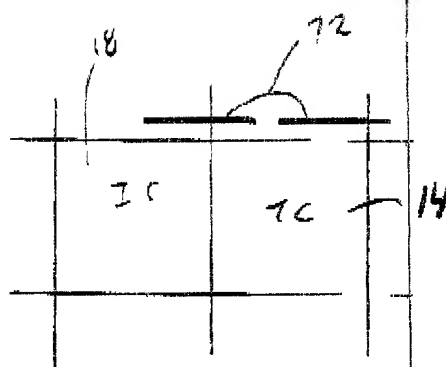


FIGURE 9b

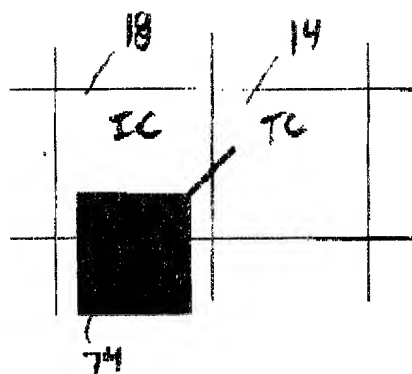


FIGURE 9c

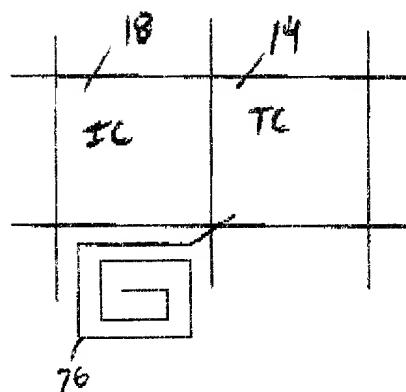


FIGURE 9d

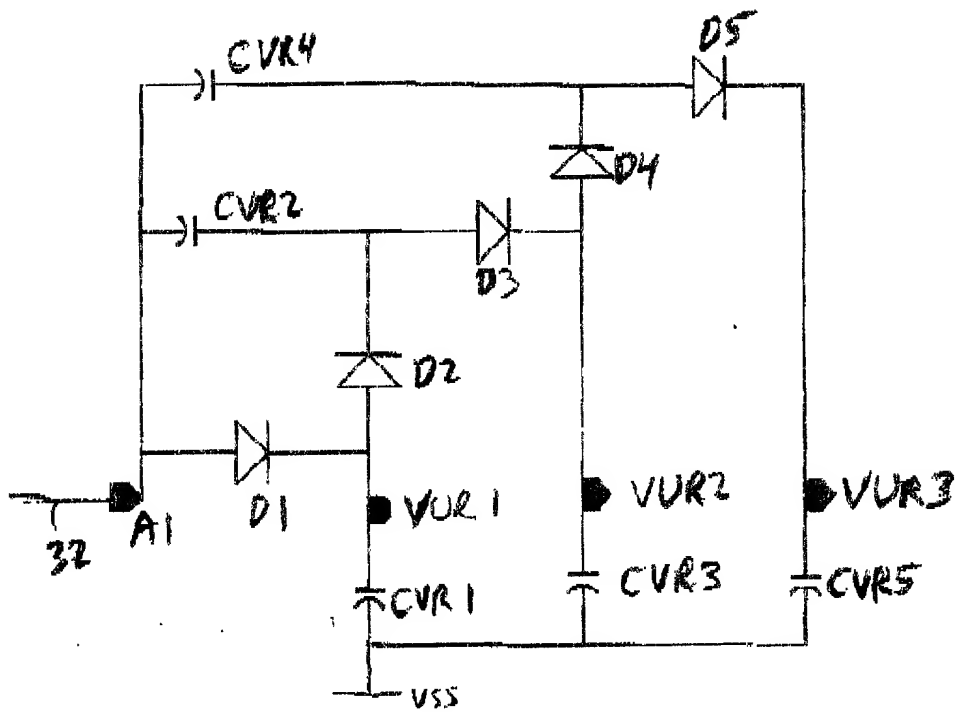


FIGURE 10.

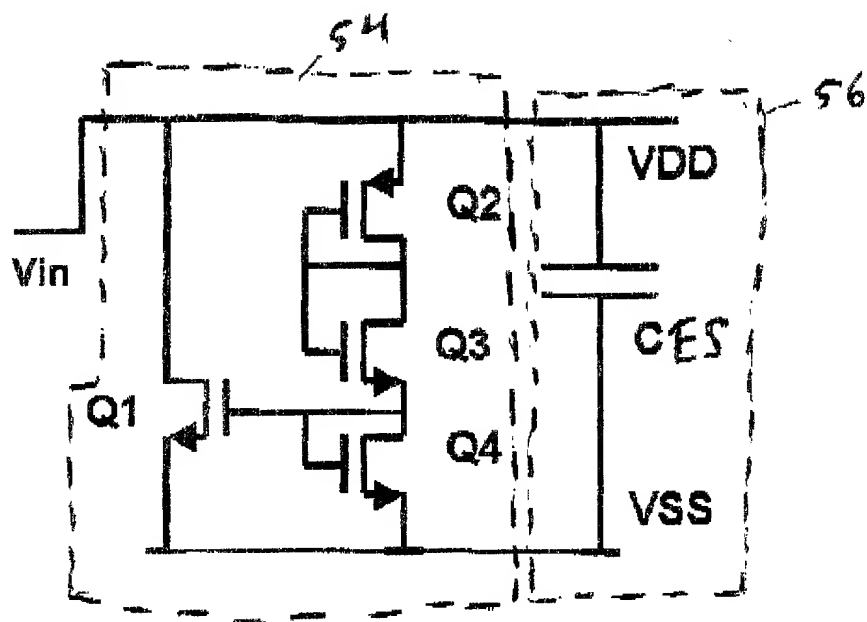


FIGURE 11

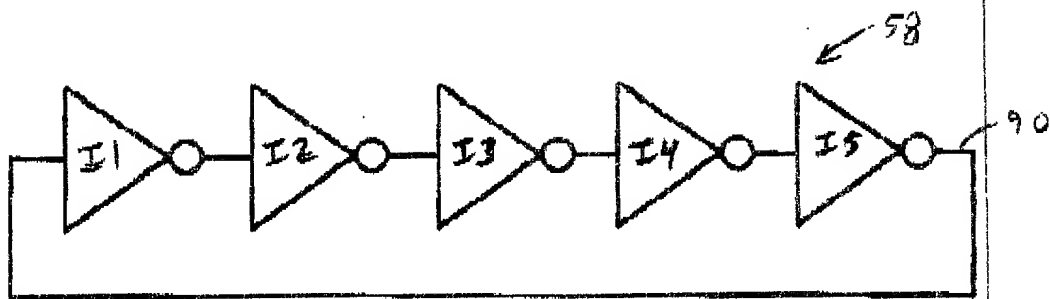


FIGURE 12

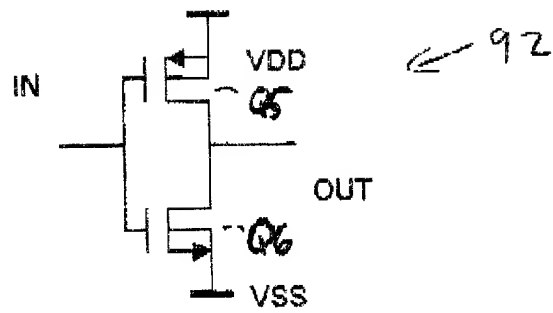


FIGURE 13

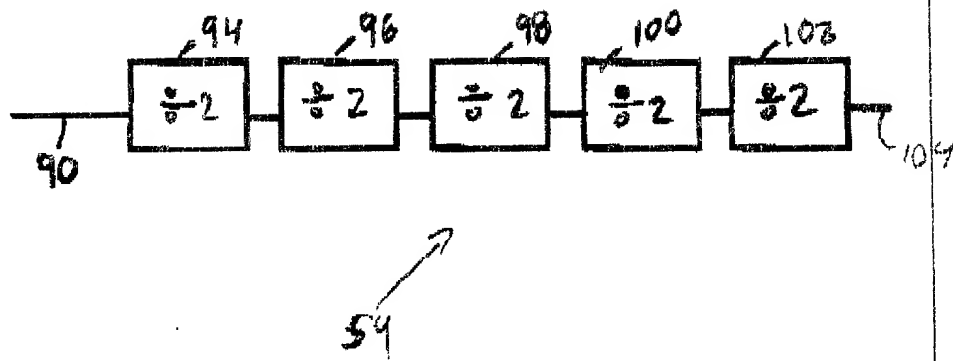


FIGURE 14.

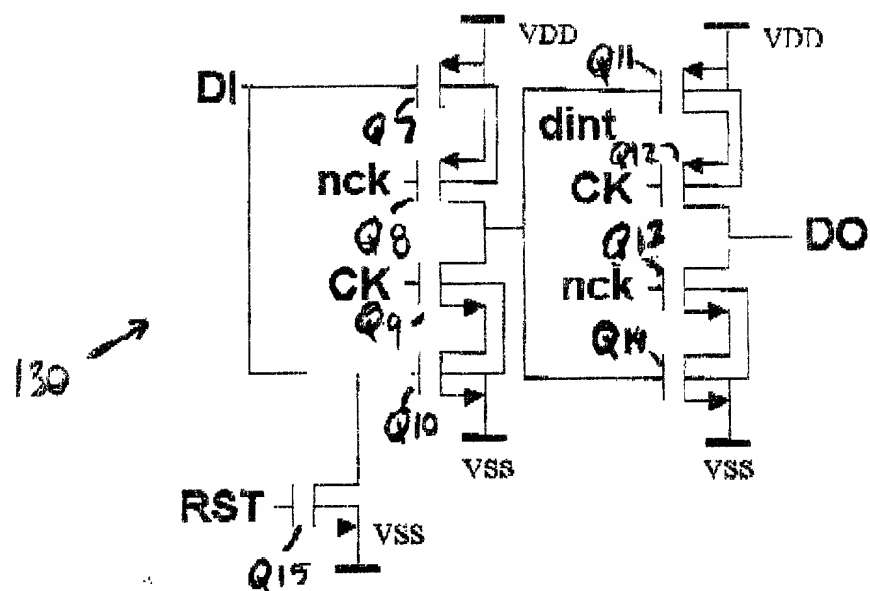
[illegible]

FIGURE 16

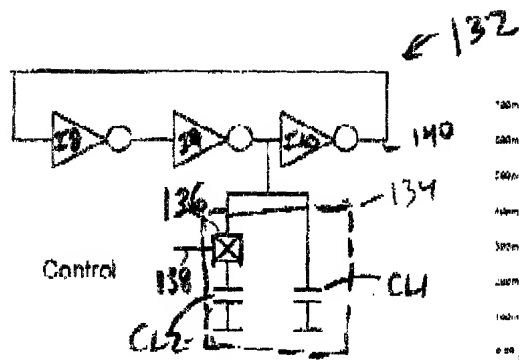


FIGURE 17a

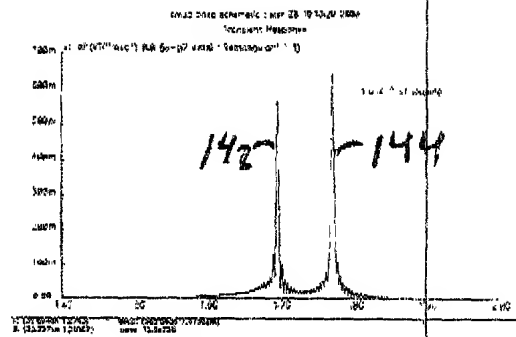


FIGURE 17b

FIGURE 17a

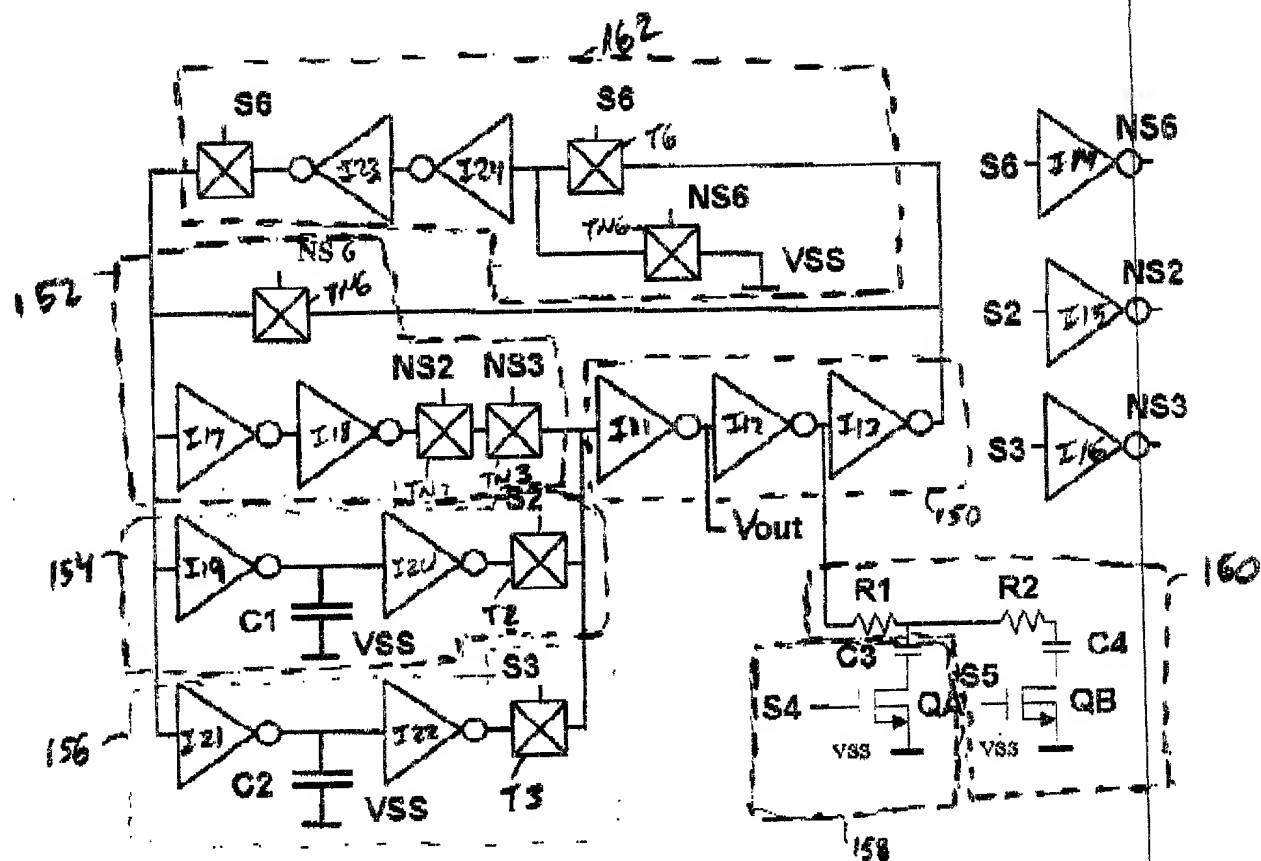


Figure 18

62

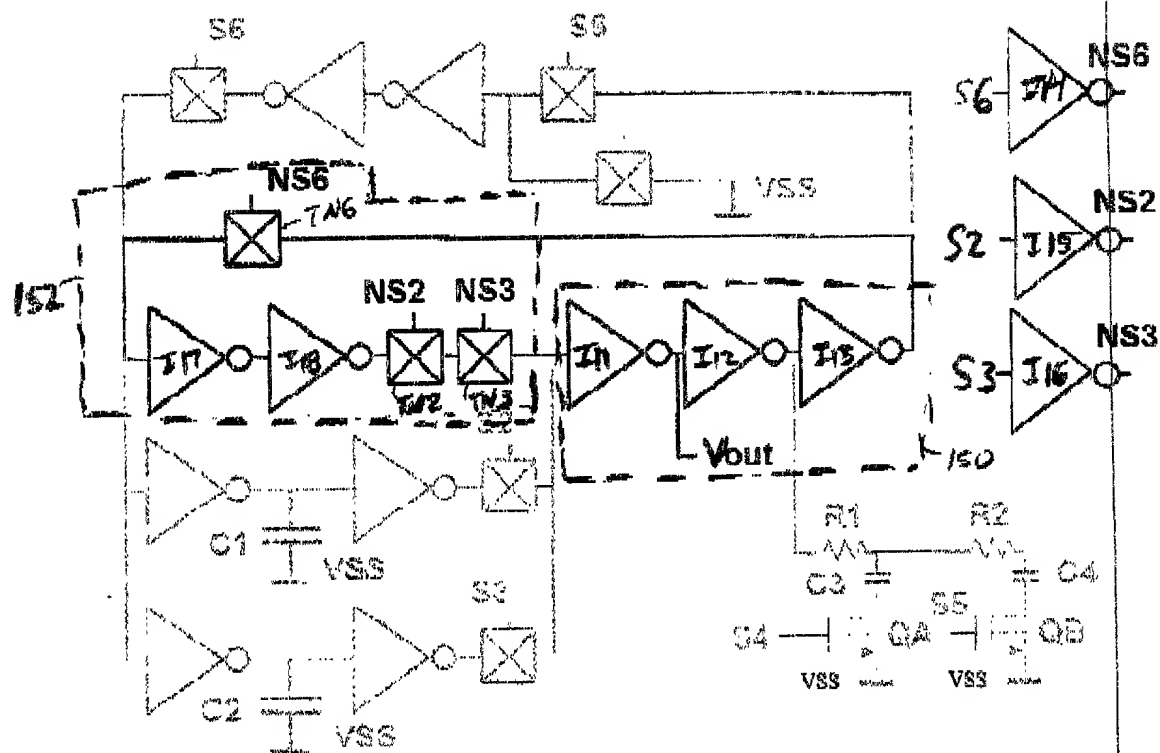
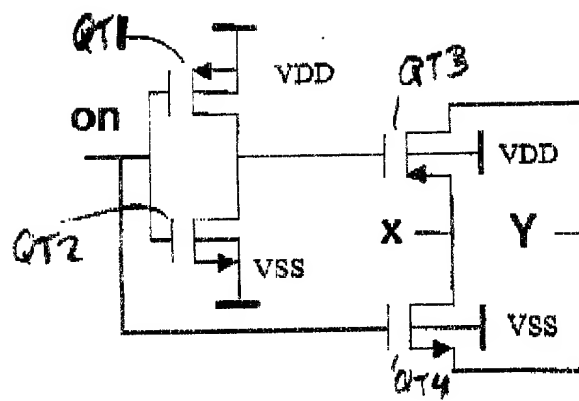


FIGURE 20

[illegible]

164

FIGURE 19

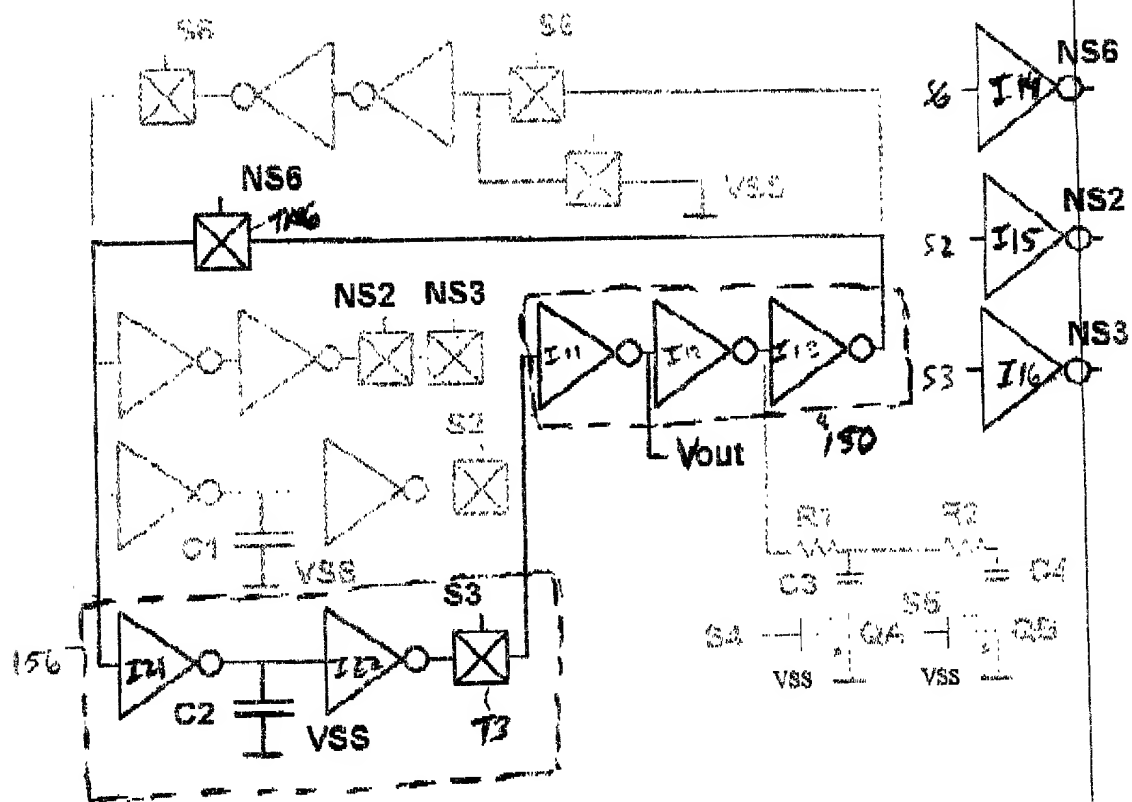


FIGURE 22

605150-9069260

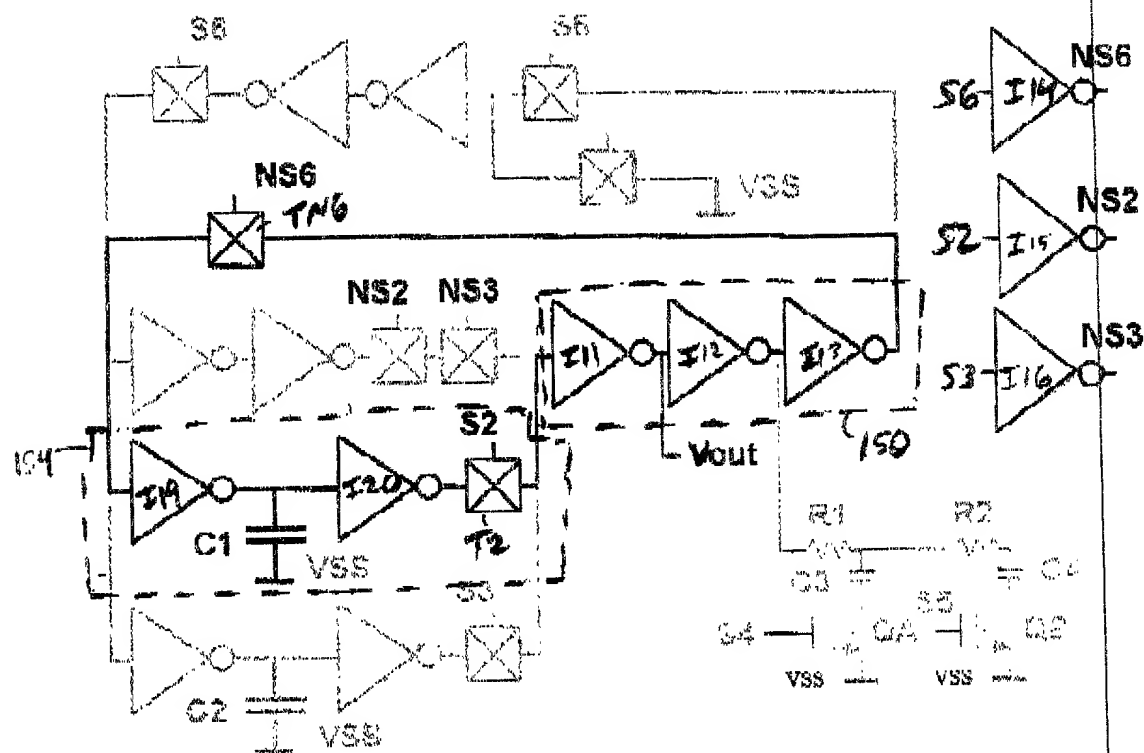


FIGURE 21

FIGURE 23

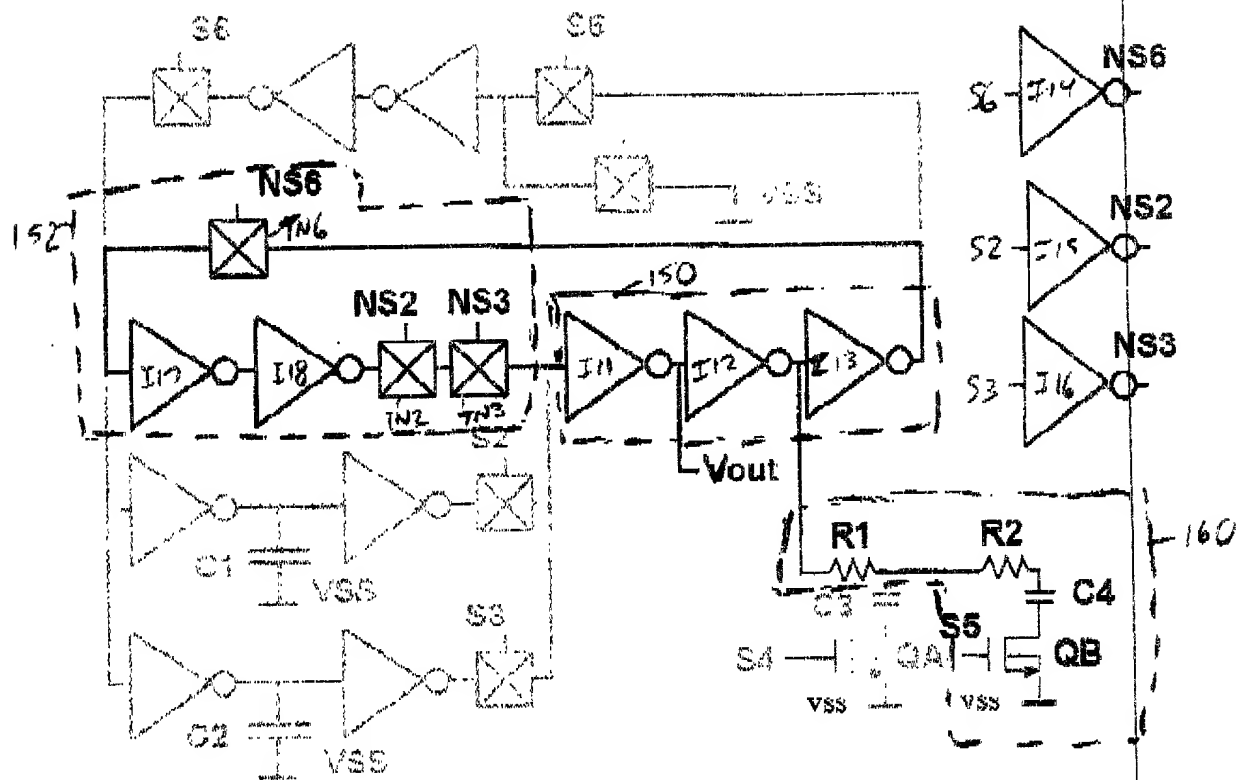


FIGURE 24

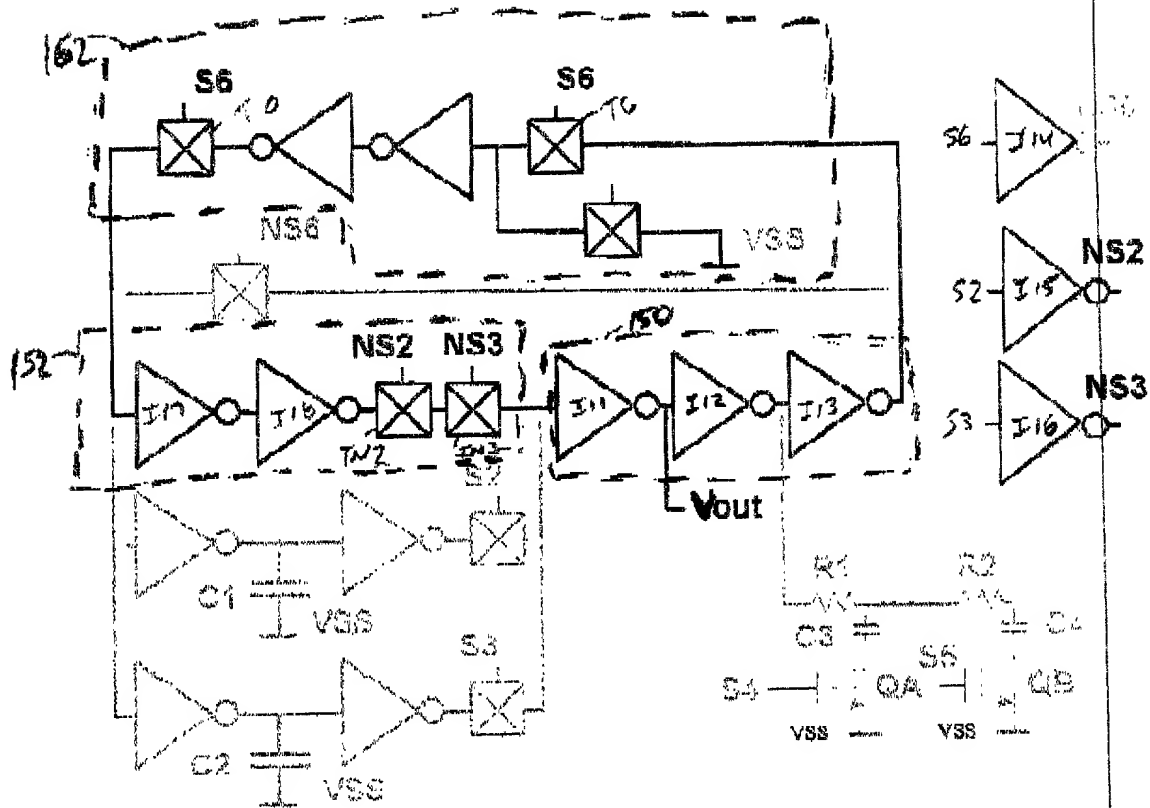


FIGURE 25

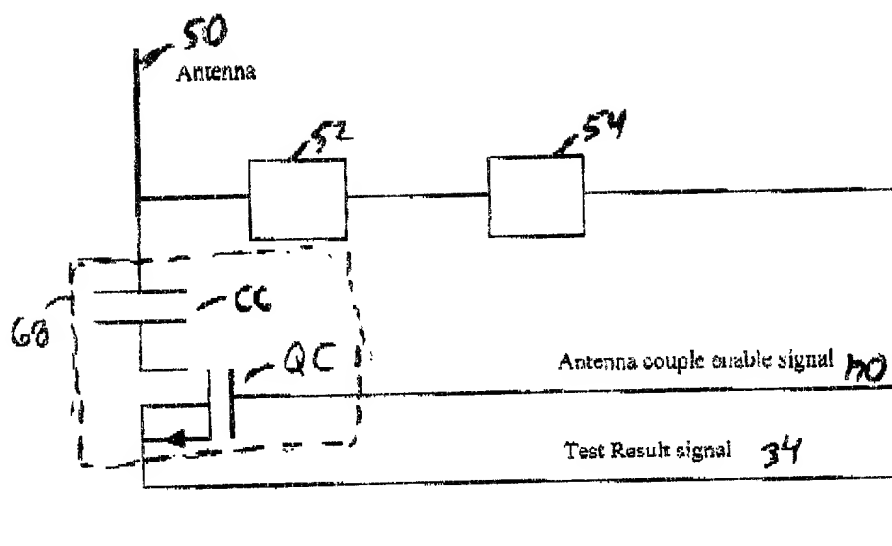


FIGURE 26

105495-5000

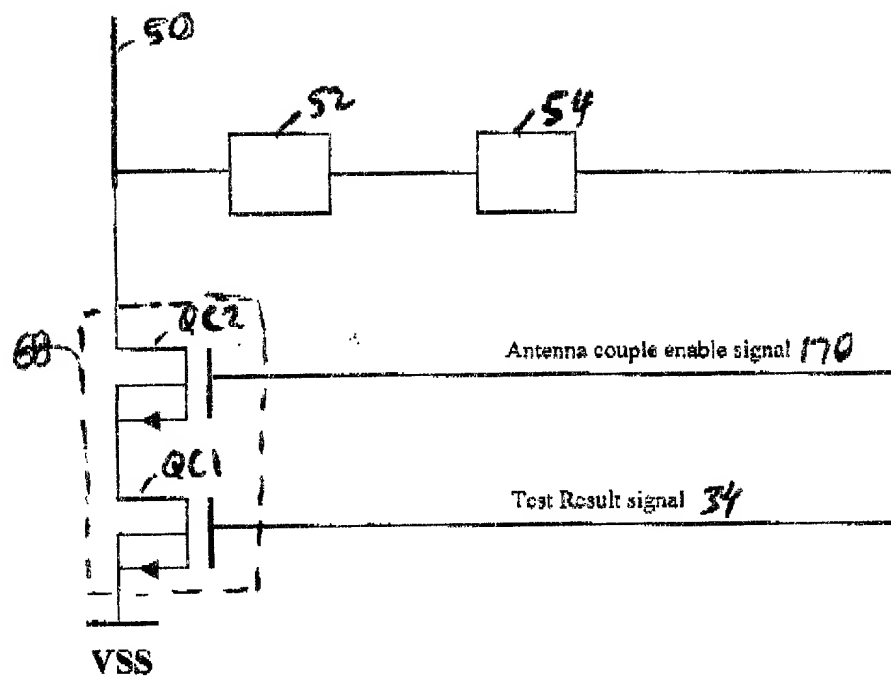


FIGURE 27

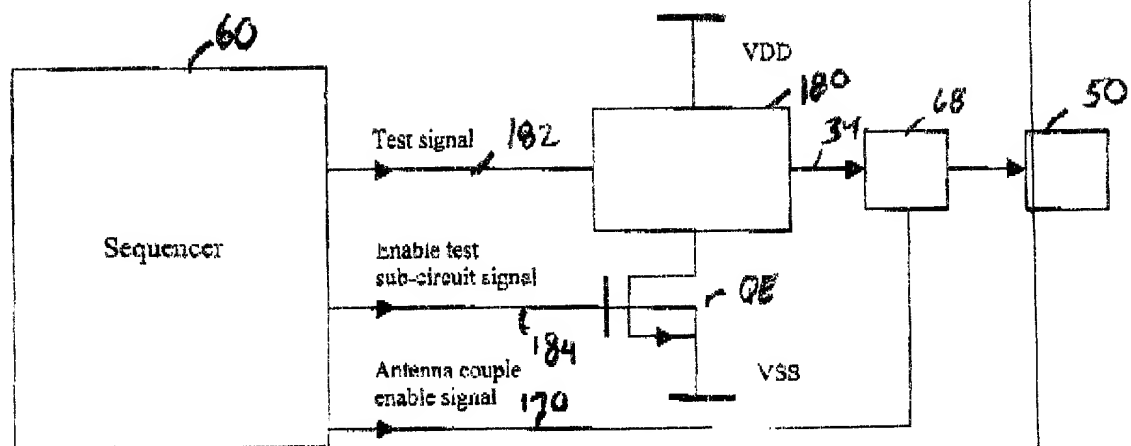


FIGURE 28

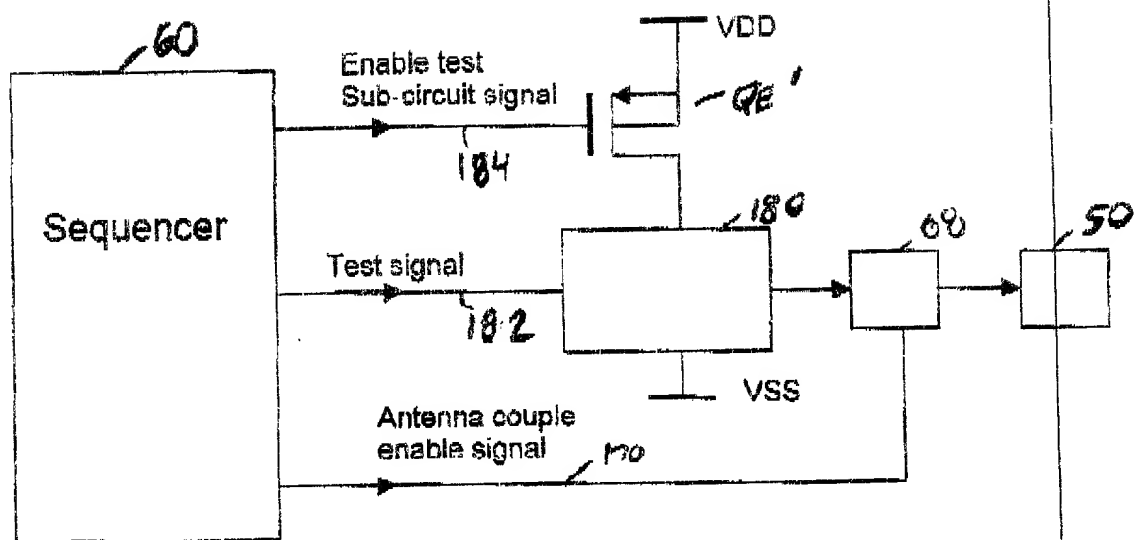
[illegible]

FIGURE 29

bm25 19_test0 schematic : Apr 11 22:46:26 2026
Transient Response

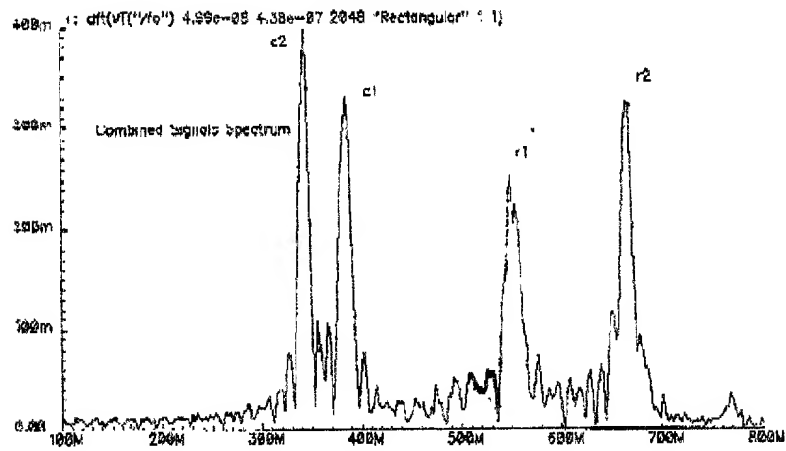


FIGURE 30a

sc582 5invaso schematic : Mar 26 19:55:38 2008

Transient Response

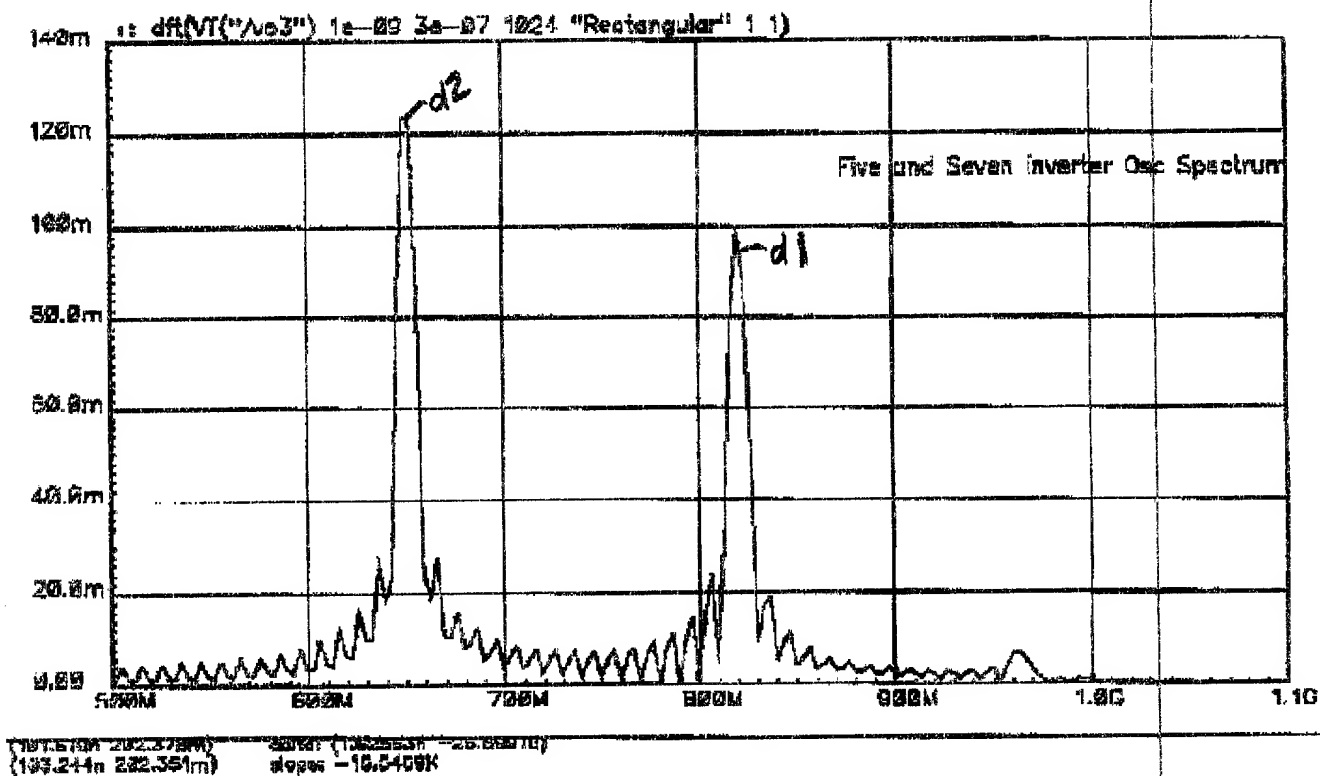


Figure 30b

FIGURE 31